Nuvoton ICP Tool User Manual

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www.nuvoton.com

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1 Overview

1.1 Introduction

Nuvoton ICP Tool is a software that can program Nuvoton Cortex®-M and 8051 1T products, which supports “online” and “offline” programming mode.

“ICP” is the acronym of In-Circuit Programming, which means user can upgrade Flash memory of MCU on board and does not need to unmount it from target PCB.

1.2 Supported Devices

Download revision history from nuvoton website to see the table of supported devices.

Users can download datasheet from *http://www.nuvoton.com*

1.3 Features

- In-Circuit programming target chip
- Online/offline programming mode
- Backup Flash data of target chip (If it is unprotected)
- Backup offline SPI Flash data of Nu-Link adapter (if it is unprotected)
- Write serials number (SN) to target chip
- Limit the maximum programming counts
- Data encryption for online/offline programming
- Batch mode for online/offline programming

2 Preparing for ICP Tool

2.1 System Requirements

The hardware and software requirements for installing the ICP Tool system are as follows:

- PC/AT compatible machine with Pentium or higher CPU
- XVGA(1024*768) color monitor
- At least 512MB RAM for best performance
- At least 10GB free disk space
- Windows 7/10 or higher

2.2 Hardware Installation

Connect PC host and Nu-Link with USB cable.

Connect Nu-Link and target chip with debugging interface.

2.3 Software Installation

Please run the installer package to install the software.

After installing the package, launch ICP Tool and you can see the dialog below.

Figure 2-1 shows the selection form at startup of the ICP Tool and Figure 2-2 shows the main window of the ICP Tool.

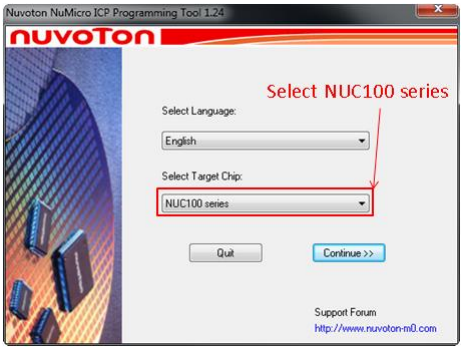

Figure 2-1 Chip Series and Language Selection

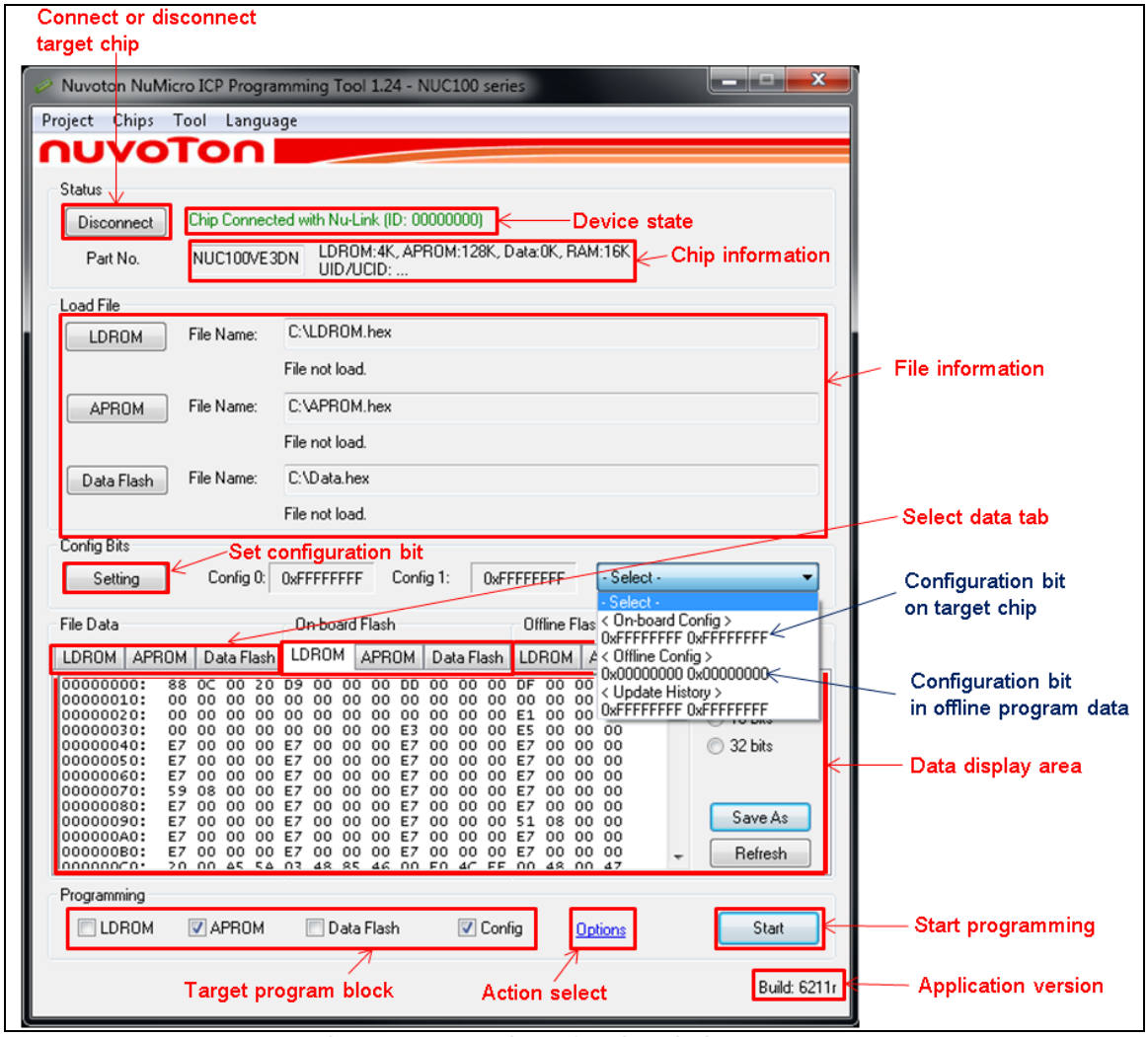

Figure 2-2 Overview of Main Window

3 Operation Modes of ICP Tool and Nu-Link Adapter

This chapter describes operation modes of the ICP Tool and Nu-Link adapter.

3.1 ICP Tool Modes

The ICP Tool supports “online” and “offline” programming mode.

3.1.1 Online Programming Mode

If the “**Offline programming mode**” option in Figure 4-13 is unchecked, ICP Tool will program in online programming mode.
Click ICP Tool “**Start**” button in Figure 2-2 to start to program target device. Target device must be connected to Nu-Link.

3.1.2 Offline Programming Mode

If the “**Offline programming mode**” option in Figure 4-13 is checked, ICP Tool will program in offline programming mode.

First, click ICP Tool “**Start**” button to program the data to SPI flash of Nu-Link. (For some part numbers, target device must be connected to Nu-Link in this step.)

Next, you can do offline program without ICP Tool, just press the button on the Nu-Link adapter (see Figure 3-1).

Note: You can refer “3.2 Code Protection in ICP Offline Mode” in “AN0001_NuMicro_Cortex-M_Code_Protection” for detail steps.

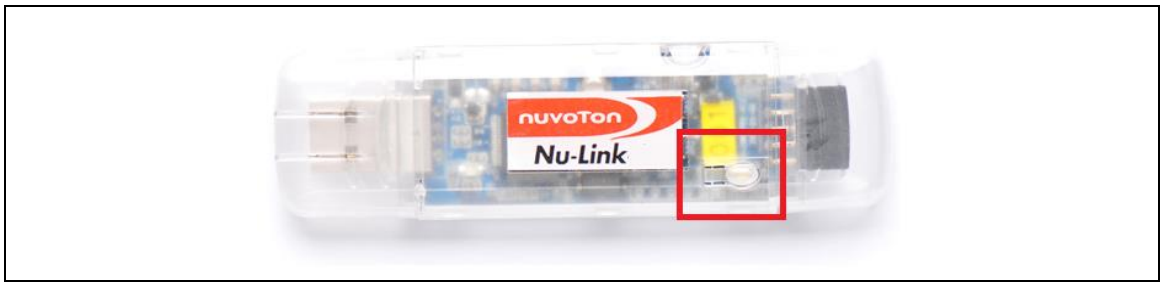

Figure 3-1 Offline Programming Button

Nu-Link, Nu-Link-Pro and Nu-Link2-ME only have SPI flash to save offline data.

For Nu-Link2-Pro, offline data can be saved in three different places.
1. USB flash drive
2. SD card
3. SPI flash

The sequence above is also the priority of detecting interface for Nu-Link2-Pro offline download.

Follow the steps below to use USB flash drive or SD card interface for offline download:
1. Use ”Tool” -> “Create Offline USB/SD File” on the menu bar to save “NuLink2.us” file and drag and drop the file into USB flash drive or SD card.
2. Plug USB flash drive or SD card into Nu-Link2-Pro adapter.
3. Pressing the button on the Nu-Link2-Pro adapter will switch the Nu-Link2-Pro to offline download mode and start to download the offline data to target chip immediately.

3.2 LED status of Nu-Link Adapter

Nu-Link Adapter Operation Status	Status LED			
	ICE	ICP	Red	Green
Boot	Flash*3	Flash*3	Flash*3	Flash*3
One Nu-Link adapter selected to connect	Flash*4	Flash*4	Flash*4	On
ICE Online (Not connected with a target chip)	On	Any	-	-
ICE Online (Connected with a target chip)	On	Any	-	On
ICE Online (Failed to connected with a target chip)	On	Any	Flash	On
During Offline Programming	-	On	-	Flash Slowly
Offline Programming Completed	On	-	-	-
Offline Programming Completed (Auto mode)	On	On	-	-
Offline Programming Failed	On	Flash	-	-

Table 3-1 Status LEDs List

4 Starting to Use ICP Tool

This chapter introduces the general operations in ICP Tool. Please refer to Figure 4-1.

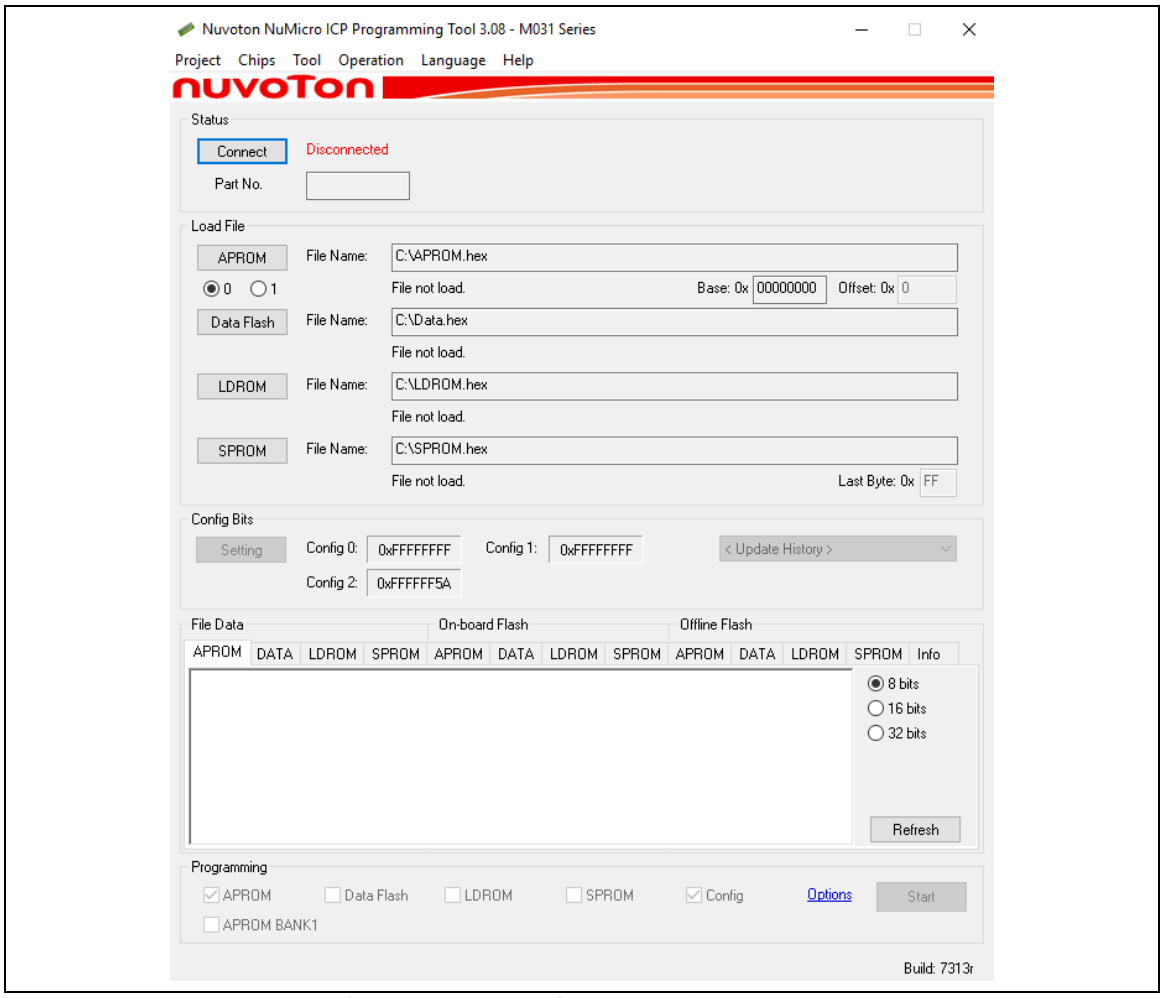

Figure 4-1 ICP Tool User Interface

4.1 Menu Bar

The menu bar is described below.

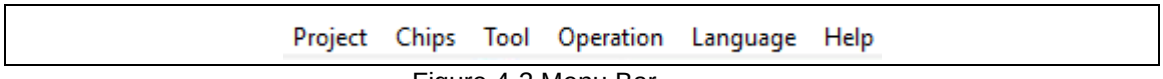

Figure 4-2 Menu Bar

- **Project**
  - Import and export the ***.icp** project file. This command can not only save and load user settings, but also do binary code protection through exporting with a certificate.
- **Chips**
  - To switch between different chips.
- **Tool**

- **Create Nu-Link certificate file**
  - For ICP Tool online mode, user can create a certificate for a specific Nu-Link adapter. Please see the “Code Protection (Online Programming Mode)” chapter for more details.
- **Create offline USB/SD file**
  - This is only for Nu-Link2-Pro, refer to section 3.1.2.
- **Merge file tool**
  - Launch an utility that can merge several bin files to one.

- **Operation**
  - **Erase offline data**
    - Erase offline data that saved in SPI flash of NuLink adapter.
  - **Erase whole target chip**
    - Enforce to erase whole target chip flash in any case.
  - **Hardware Reset Target Chip**
    - Use hardware reset target chip by NuLink adapter.
  - **System Reset Target Chip**
    - Use system reset target chip by NuLink adapter.

- **Language**
  - Switch between “English”, “Simple Chinese” and “Traditional Chinese”.

- **Help**
  - **Version check**
    - To check if there is any new release version on Nuvoton web site.

4.2 Connection Status

- Before connected

The ICP Tool will try to connect target chip once the user clicks the “**Connect**” button.

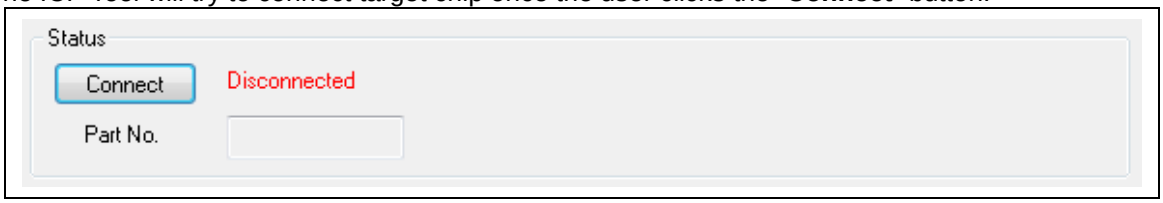

Figure 4-3 before Connected Status

- After USB adapter connected successfully

The ICP Tool shows “ICE Connected”.

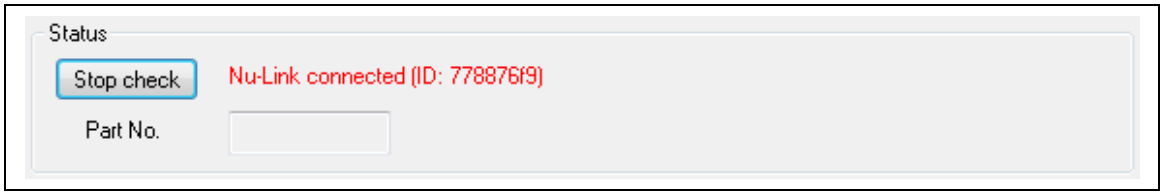

Figure 4-4 after USB Adapter Connected Status

- After target chip connected successfully

The ICP Tool shows chip information.

Figure 4-5 after Target Chip Connected Status

4.3 Load File

- Select file for programming

Select a file for programming. Then the file size and checksum information will be displayed.

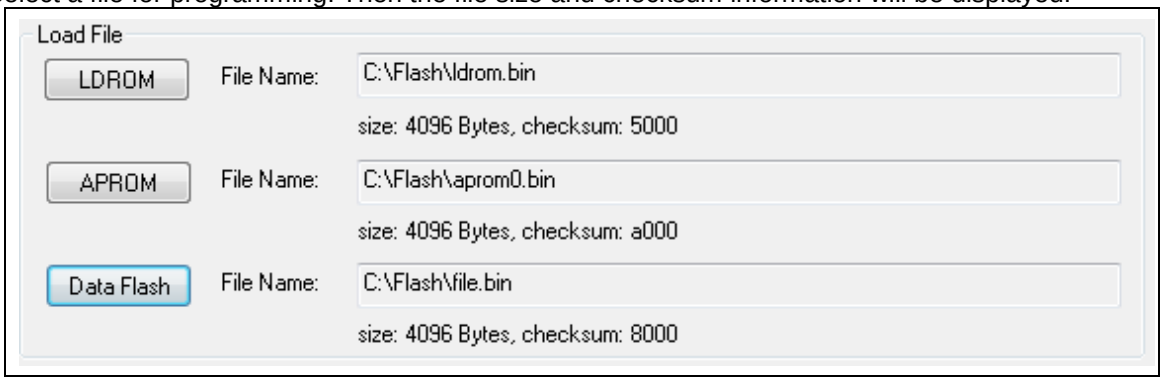

Figure 4-6 Select File for Programming

- Supported file format:

The supported file format includes **bin** or **Intel hex** (Intel 8, 16 and 32) file format.

4.4 Configuration Bits

The process is to erase the selected block first and program the flash block separately and perform the verification action.

The flash block burning sequence is => Configuration => APROM => LDROM => Data Flash.

The Config Bits section is used for configuration bits setting.

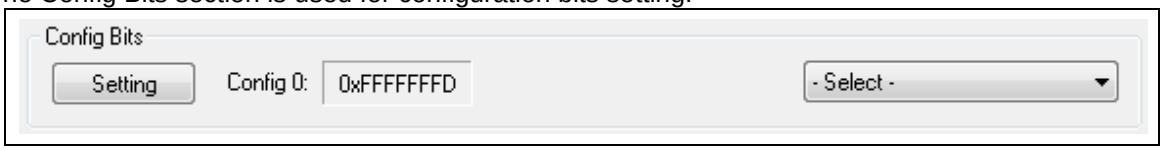

Figure 4-7 User Configuration Status

The Chip Options form will be displayed after clicking the “**Setting**” button.

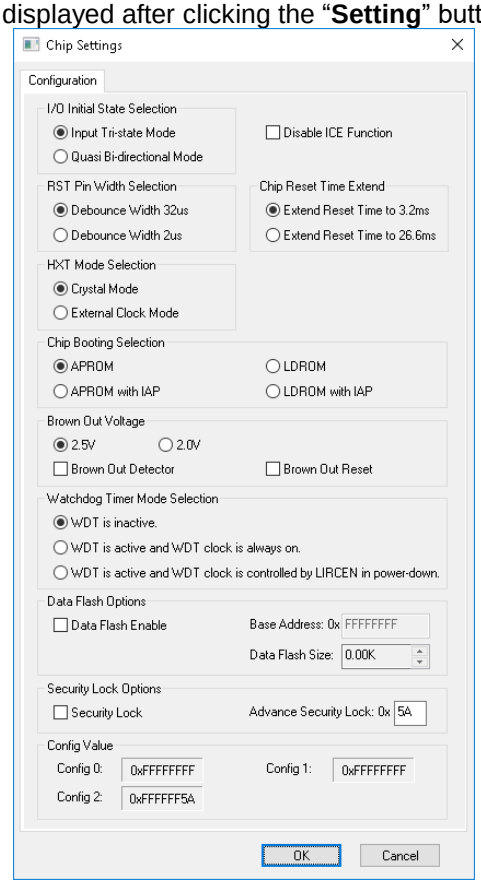

Figure 4-8 User Configuration Settings

- Configuration selection list
  - **"On-board Config"** shows the configuration bit that’s read from target chip;
  - **"Offline Config"** shows the configuration bit read from Nu-Link that’s saved previously for offline programming;
  - **"Update History"** shows the history configuration.

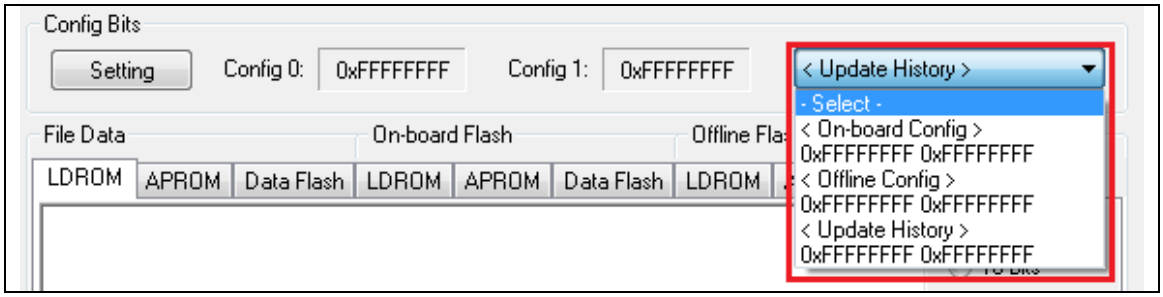

Figure 4-9 User Configuration Setting History

4.5 Dump Data

Once refresh, the data information section will show three parts of Flash data information respectively, including “File Data”, “On-board Flash”, and “Offline Flash”.

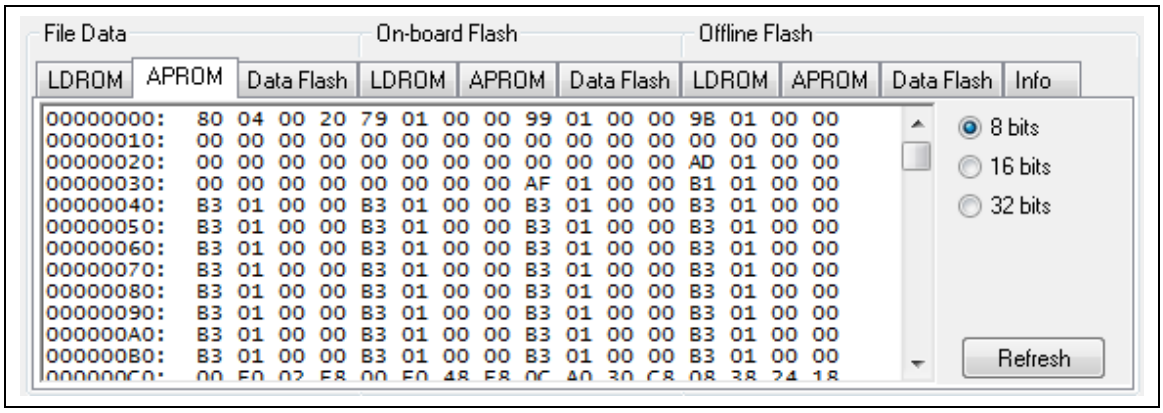

Figure 4-10 Data Information Interface

- File data
  - The file content selected in the “Load file” group.

- On-board Flash
  - The data programmed on built-in Flash of target chip.

- Offline Flash
  - The offline data on SPI Flash of Nu-Link adapter.
  - The info tab will show download information of online and offline programming mode

4.6 Programming Options

Figure 4-11 Programming Options and Start Button

- Target programming block

User can program APROM, Data Flash, or LDROM separately.

In online/offline programming mode, user needs to set the target programming block as shown in Figure 4-12. And user can select program, verify and erase action in program option as shown in Figure 4-13.

If user select the “Erase Whole Chip” option as shown in Figure 4-13, the ICP Tool will erase the whole target chip (APROM, LDROM, Data flash and config bits).

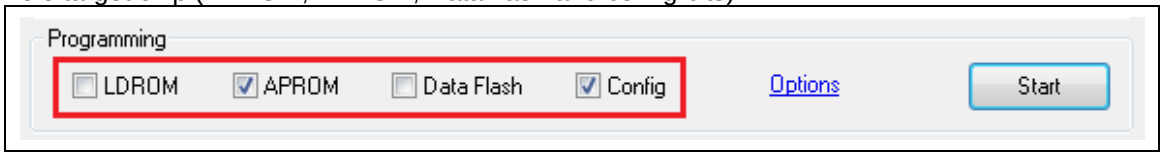

Figure 4-12 Programming Blocks

- Programming Options

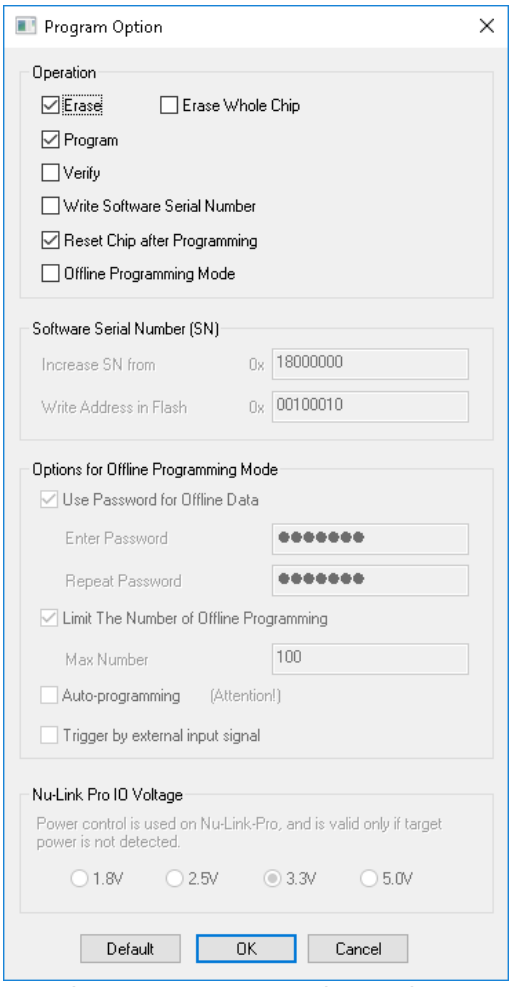

Figure 4-13 Programming Options

- The operation group contains erase, program, verify, offline programming mode option settings.
- User can enable “Write Software Serials Number”, and assign “SN start value” and “target flash address where SN saved”.
- User can specify the password for offline programming mode and the limitation of maximum programming count for security issue.

4.7 Programming Status

The ICP Tool has progress bar and program status. After programming is done, a dialog box will display the success or fail information.

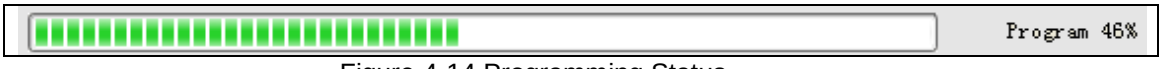

Figure 4-14 Programming Status

5 Code Protection (Online Programming Mode)

This chapter describes the code protection in online programming mode. (referring to Figure 5-1).

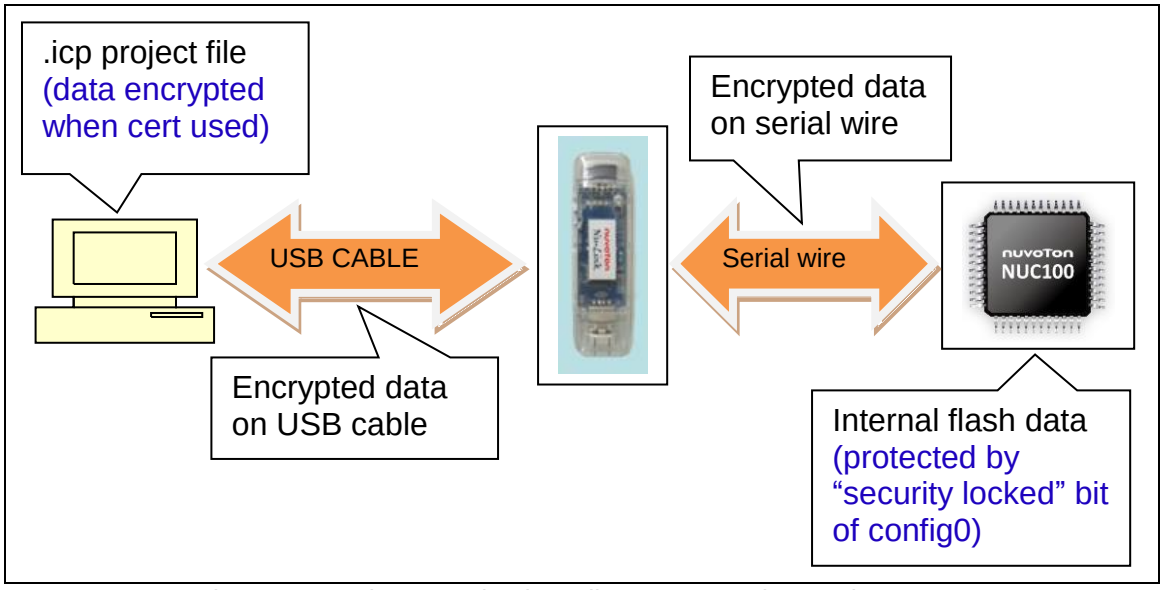

Figure 5-1 Code Protection in Online Programming Mode

5.1 Introduction to Nu-Link Certification

Each Nu-Link can create its own certificate, developer can protect his code by Nu-Link certificate mechanism.

5.2 How to use Nu-Link Certification

Step 1: Factory side creates Nu-Link's certificate and sends it to developer.

Step 2: Developer encrypts his code with this certificate and exports a *.icp project file, then sends the *.icp file back to Factory.

Step 3: Factory side imports the .icp file. Only the certificate-creating Nu-Link adapter can authenticate itself to ICPTool. Each time the ICPTool successfully decrypts code and programs to target chip, it updates the programming count till the limitation.

The protections:

- If someone gets developer's *.icp project file, he can't program target chips without the specific certificate-creating Nu-Link adapter.
- The content of exported *.icp project file is encrypted.
- Factory is not allowed to do unlimited programming and production.

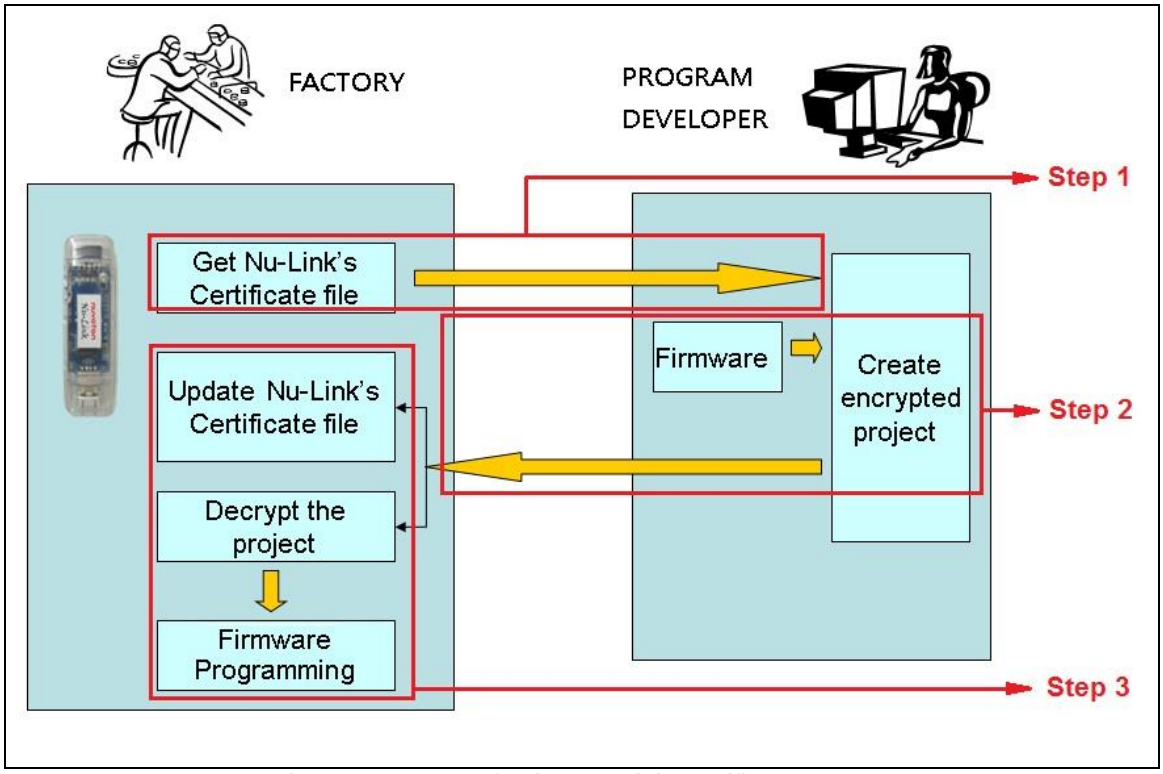

Figure 5-2 Steps of using Nu-Link Certificate

5.3 Detail steps

5.3.1 Create Nu-Link Certificate File (Factory Side)

Step 1: Connect Nu-Link.

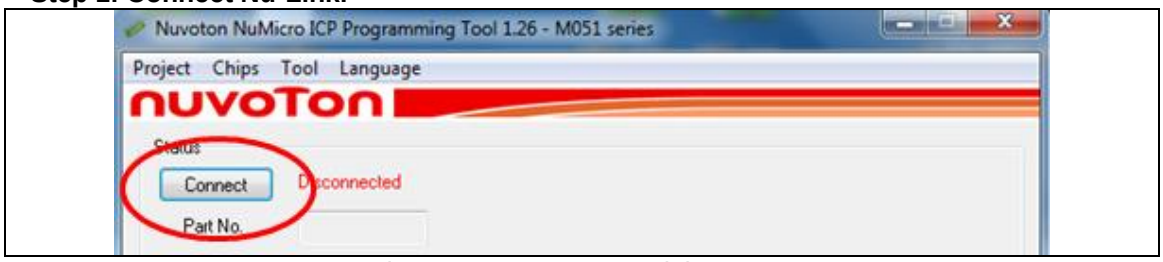

Figure 5-3 Connect Nu-Link

Step 2: Create certificate file.

Figure 5-4 Create Nu-Link Certificate File

Step 3: Save Nu-Link certificate file (*.ict).

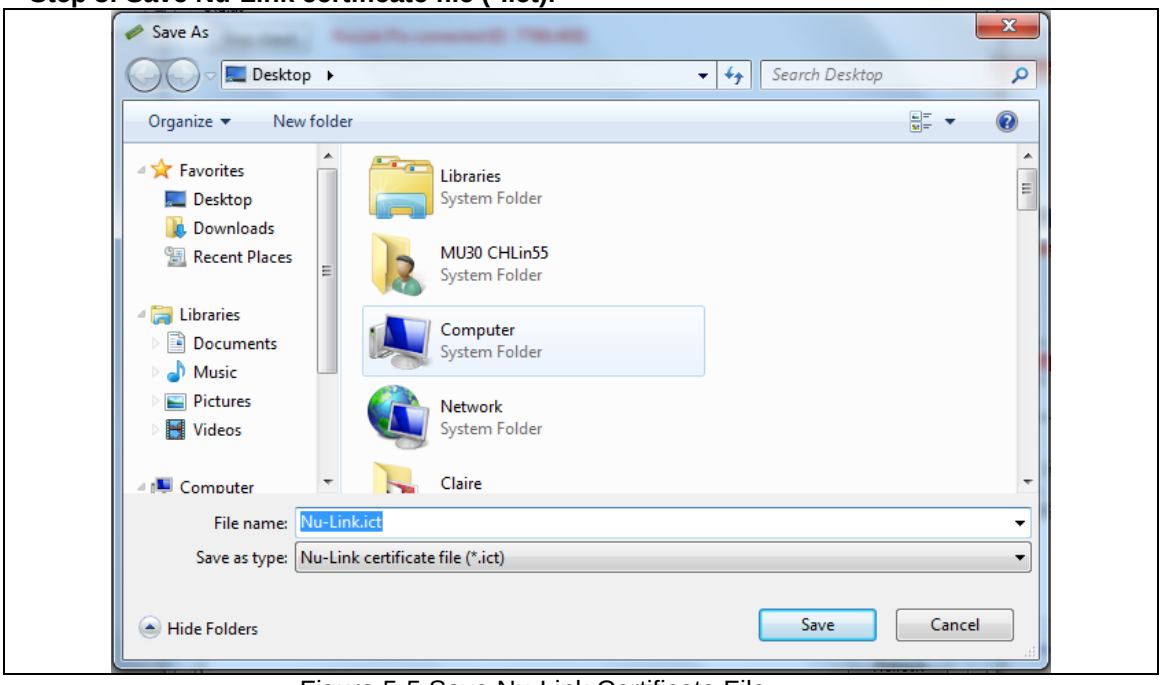

Figure 5-5 Save Nu-Link Certificate File

Step 4: Send Nu-Link certificate file to developer.

5.3.2 Bind Certificate File and Code to export Encrypted Project (Developer Side)

Step 1: Export project.

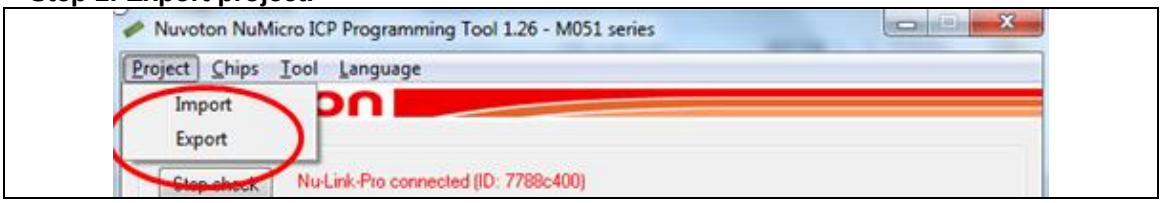

Figure 5-6 Export Project

Step 2: Enable settings.

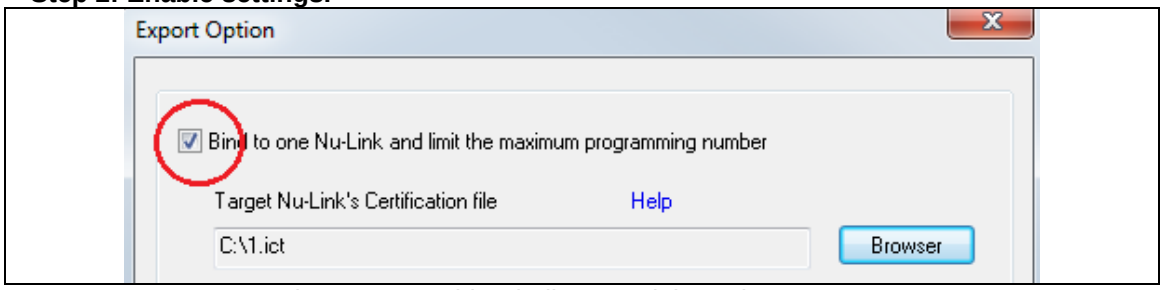

Figure 5-7 Enable Binding Nu-Link Settings

Step 3: Select the Nu-Link certificate file path.

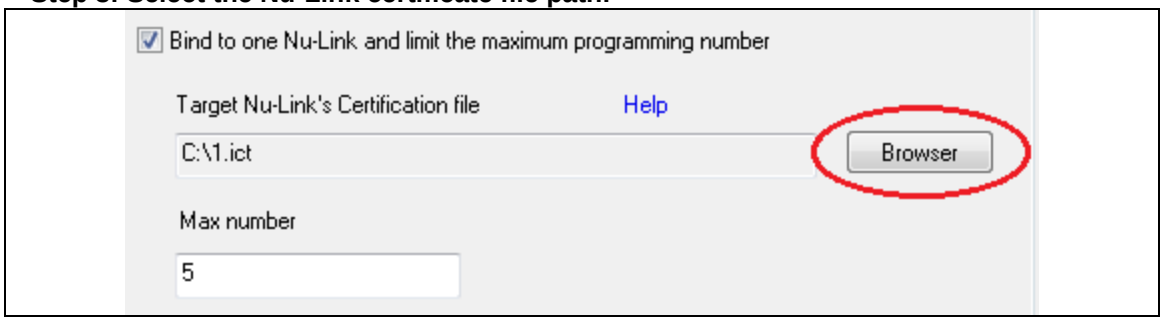

Figure 5-8 Select Nu-Link Certificate File

Step 4: Enter the maximum programming number and start to export the project.

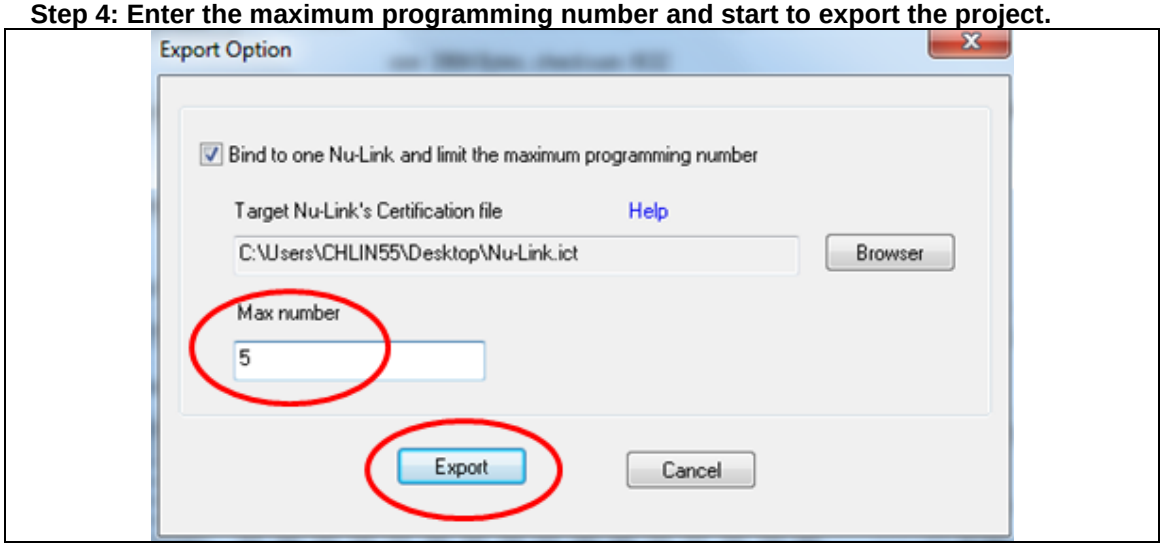

Figure 5-9 Set Max Programming Number

Step 5: Save the ICP project file (*.icp).

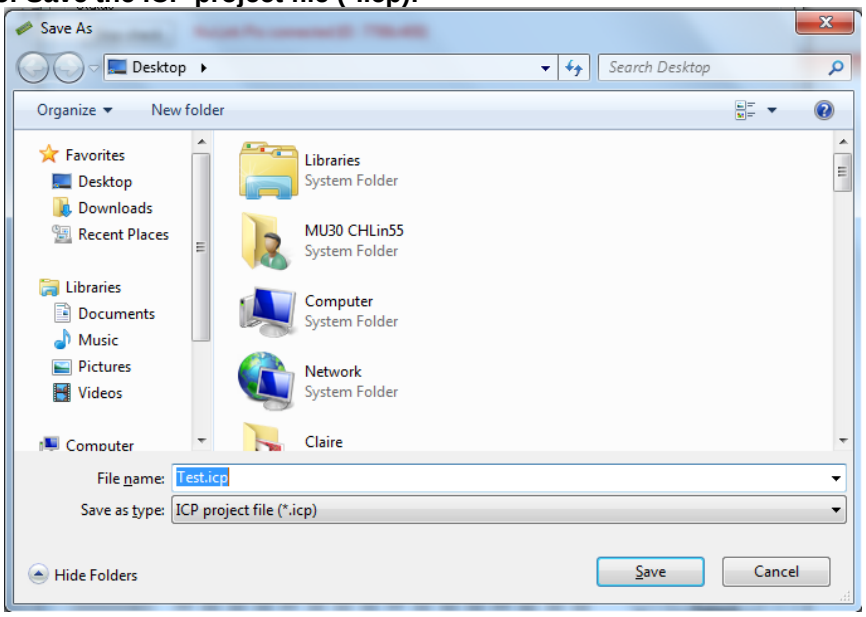

Figure 5-10 Save ICP Tool Project File

Step 6: Send back *.icp project file to Factory.

5.3.3 Import *.icp Project File (Factory Side)

Step 1: Import project.

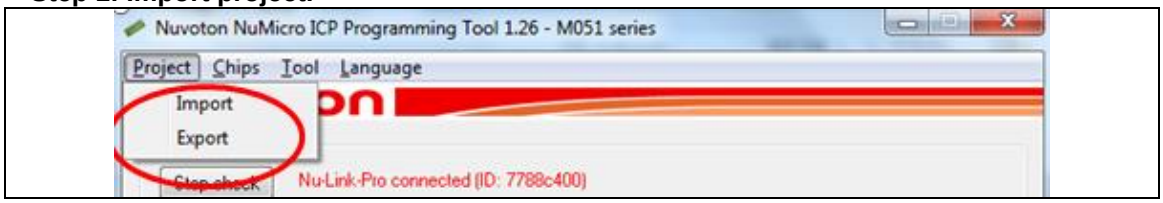

Figure 5-11 Import Project

Step 2: Press start button to program.

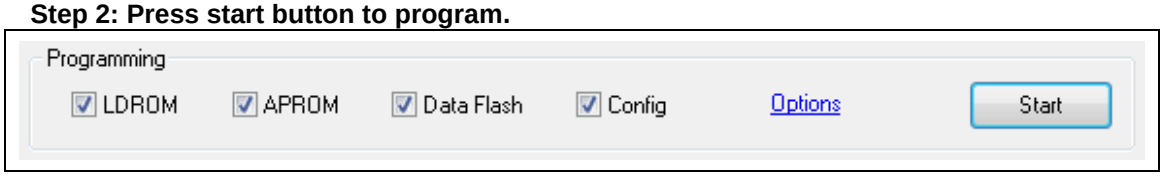

Figure 5-12 Start Button

Step 3: Programming information updates.

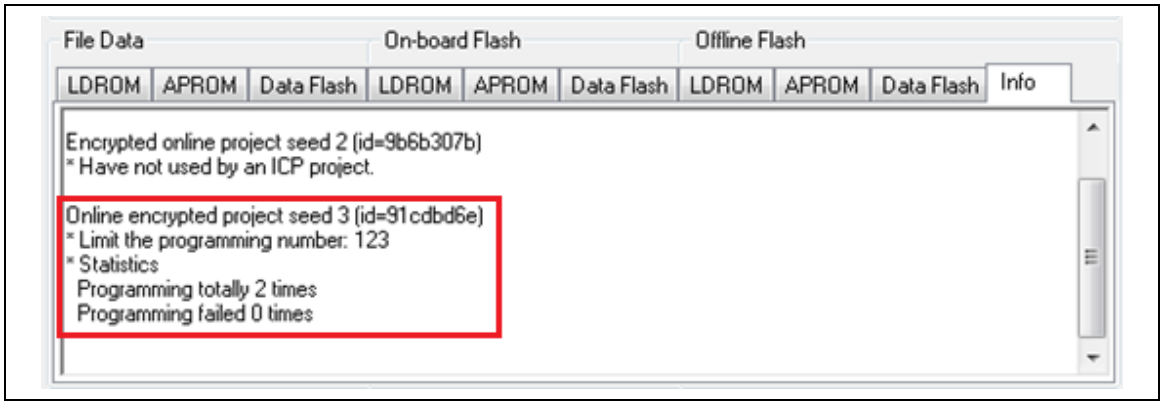

Figure 5-13 Flash Programming Information

Step 4: Factory can do production until the "Limited programming numbers".

Note: Once the project exceeds the maximum programming number, user needs to create a new Nu-Link certificate and set the new max programming number.

Note: Even user sets the maximum programming number several times, the maximum programming number is the one set at first time.

- For example:
  - Use ABC.ict to create an ICP project file and set the maximum programming number to 10.
  - After programming 5 times, use ABC.ict to create the second ICP project file and set the maximum programming number to 3.
  - The second ICP project file still can program 5 times.
  - Set the maximum programming number carefully at first time and it is better to create different certification for different ICP projects.

6 Code Protection (Offline Programming Mode)

This chapter describes code protection in offline programming mode. (referring to Figure 6-1).

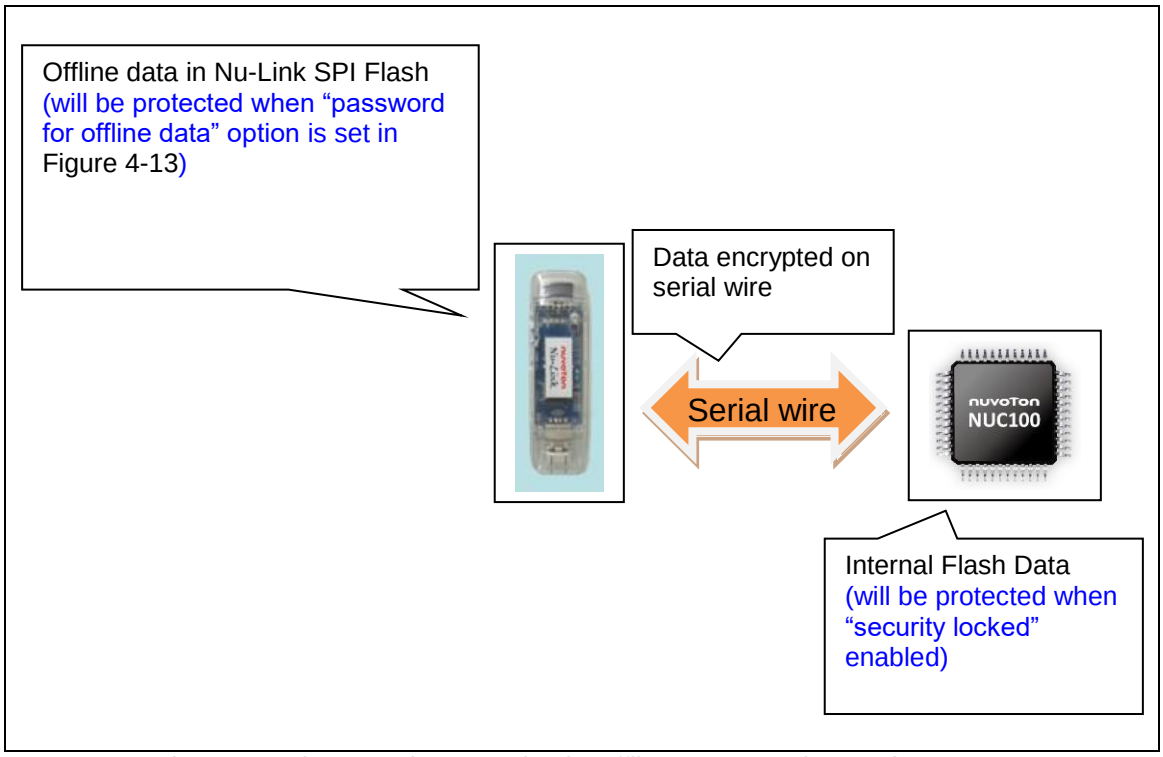

Figure 6-1 Binary Code Protection in Offline Programming Mode

For code protection, it is suggested to set the "**password for offline data**" in Figure 4-13, and enable "**Security lock**" in Figure 4-8.
To limit offline programming counts, please enable the "**Limit the number of offline programming**" option as shown in Figure 4-13.

Note: You can refer "3.2 Code Protection in ICP Offline Mode" in "AN0001_NuMicro_Cortex-M_Code_Protection" for detail steps.

7 Nu-Link Firmware Update

Follow the steps below to update firmware:

Step 1: Run the ICP Tool. Click 'Connect' and start to connect to a device. If the firmware version and driver version are not matched, a firmware update dialog will be displayed, as shown in Figure 7-1.

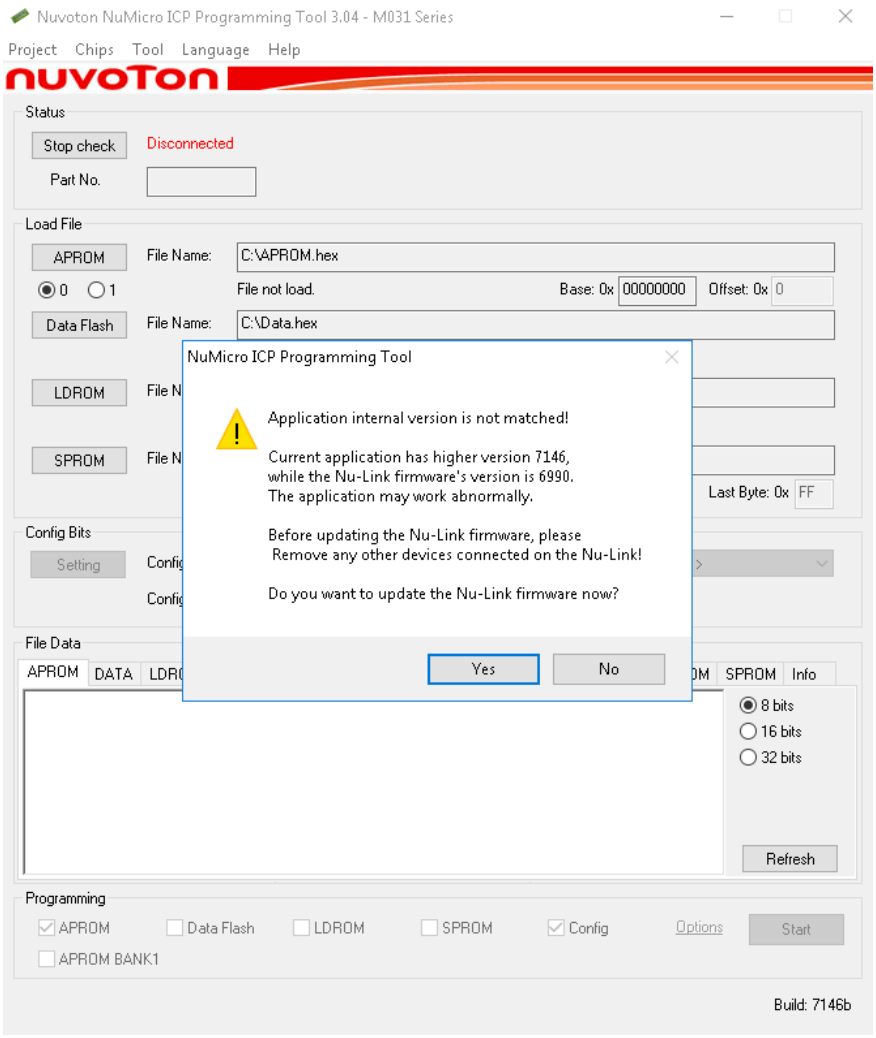

Figure 7-1 Firmware Update Selection Dialog Box

Step 2: Click "**Yes**" to update firmware, as shown in Figure 7-2.

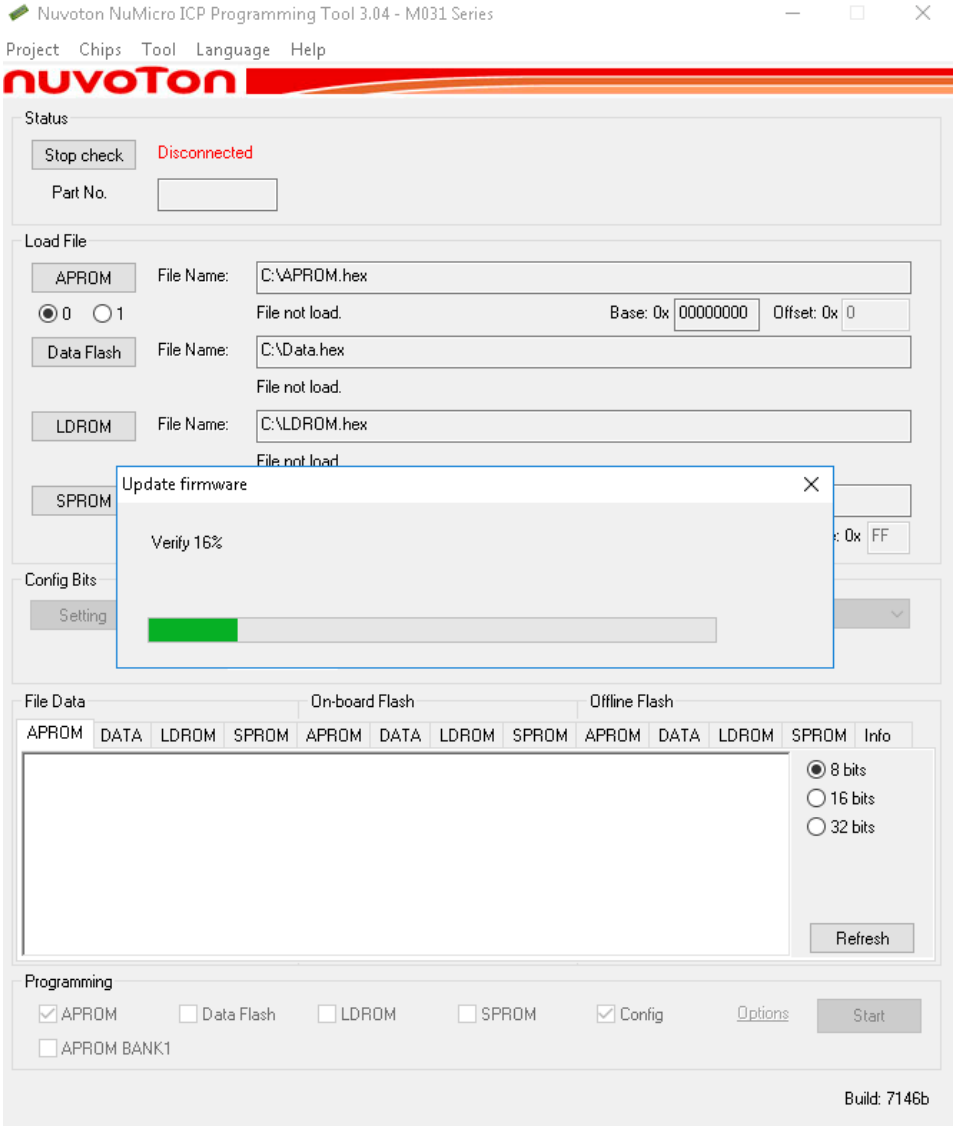

Figure 7-2 Firmware Update Dialog Box

Once the update is completed, user needs to re-connect the Nu-link to PC, as shown in Figure 7-3.

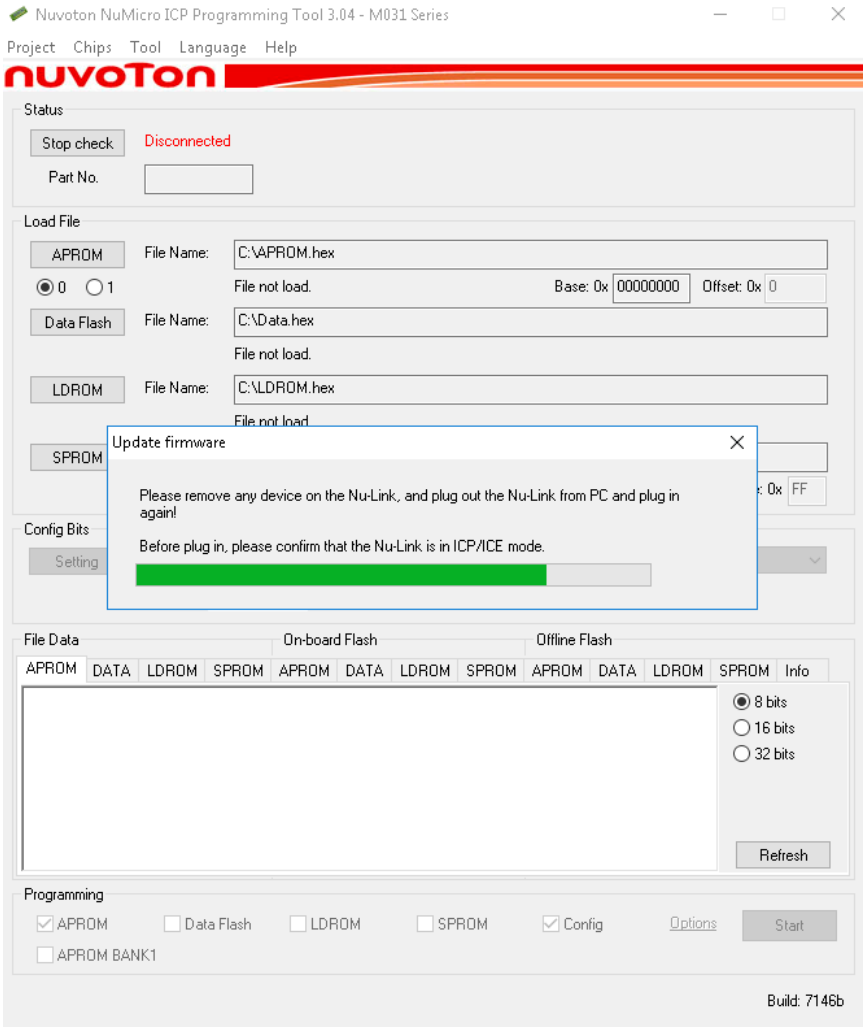

Figure 7-3 Re-connect Nu-Link to Complete Firmware Update

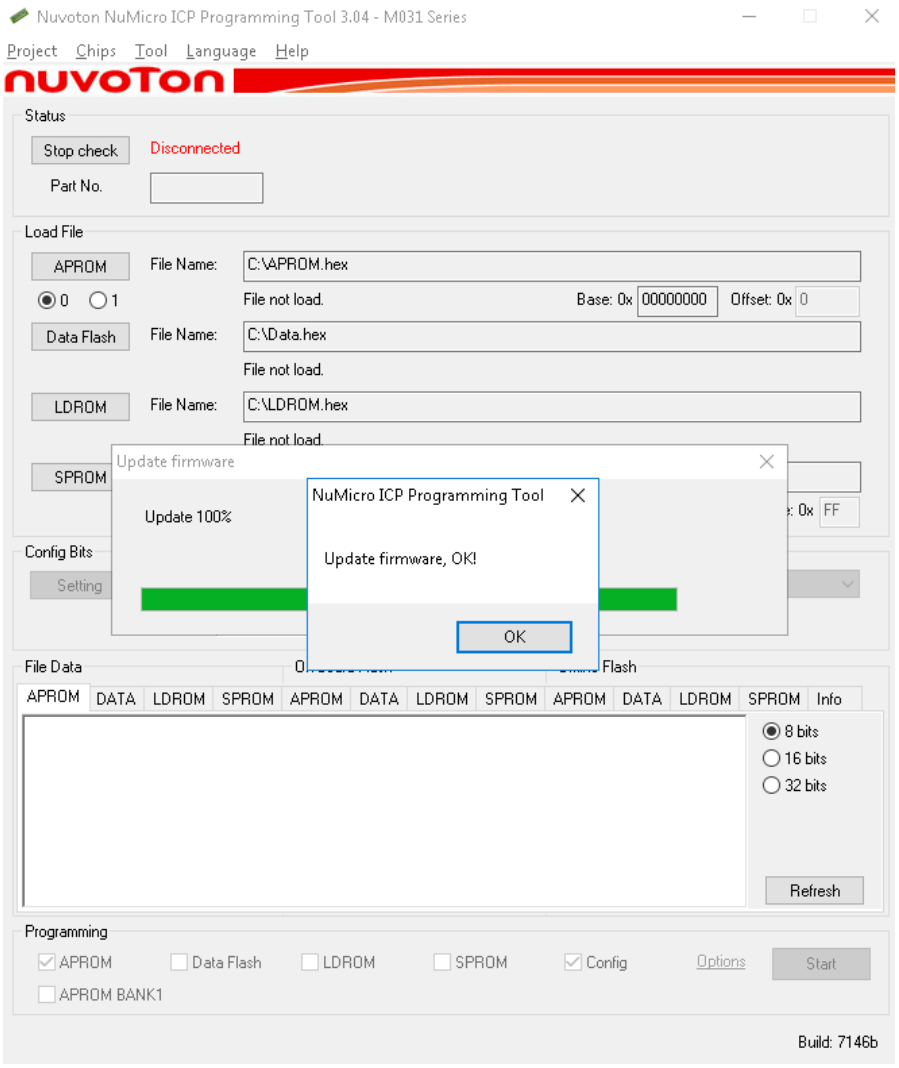

Figure 7-4 Update Firmware Completely

If you are using Nu-Link2 adapter, you can also use drag and drop method to upgrade firmware. Press the button on Nu-Link2 adapter and plug in USB cable, you will see a disk name “Nu-Link2”. Drag and drop bin file into it will upgrade Nu-Link2 firmware. (Note: if you see disk name “NuMicro MCU” it will upgrade the target device firmware instead of Nu-Link2 itself.)

For more information about the role of Nu-Link2 adapter, Please refer to the following link:

https://github.com/OpenNuvoton/Nuvoton_Tools

https://gitee.com/OpenNuvoton/Nuvoton_Tools

8 Features of Specific Series

8.1 Support for NUC505 Series

8.1.1 Main Window for NUC505 Series

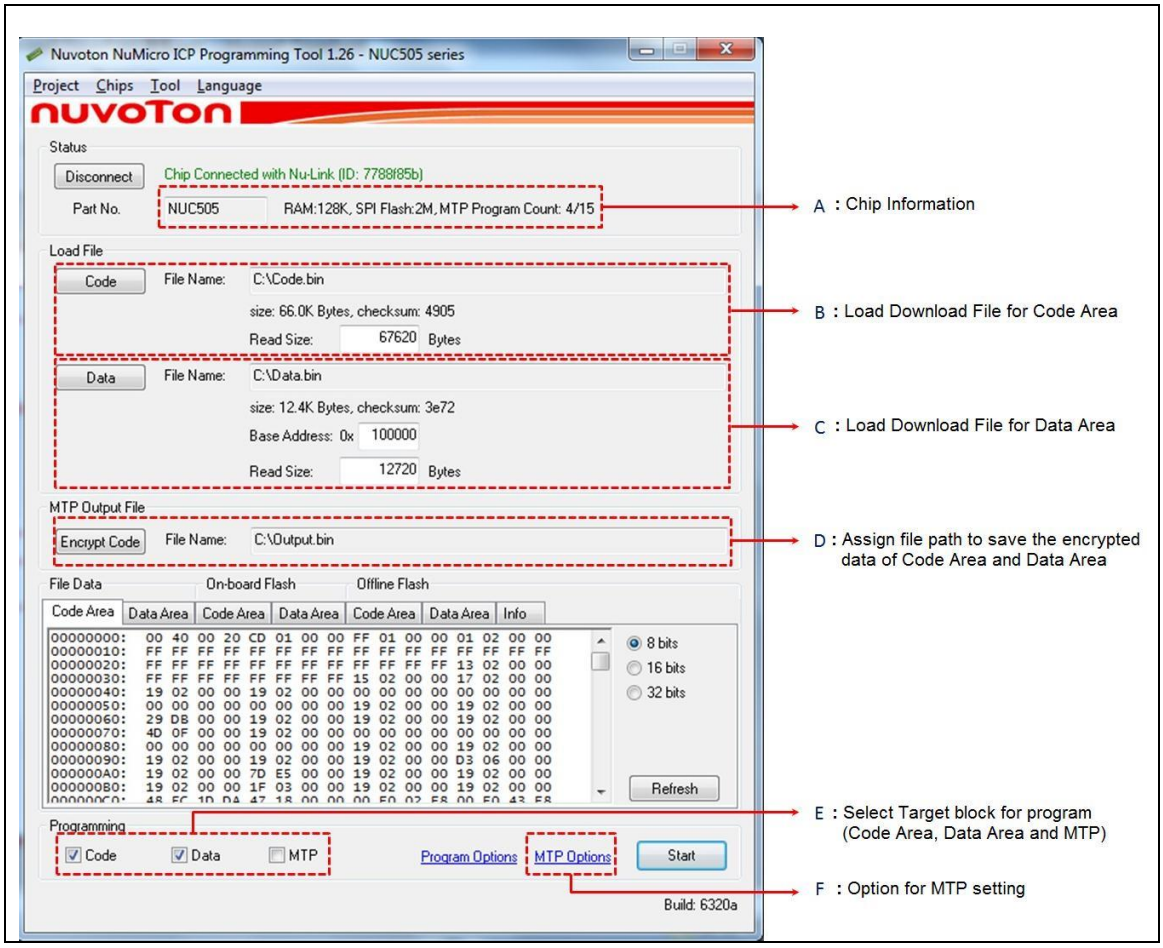

Figure 8-1 Main Window for NUC505 Series

After a target chip is detected, the ICP Tool would read chip information (including Part No., RAM size, SPI Flash size and MTP status) and show the information on section A in Figure 8-1. If MTP is locked, MTP status shows “MTP: Locked” in red and the “MTP” option is also unchecked.

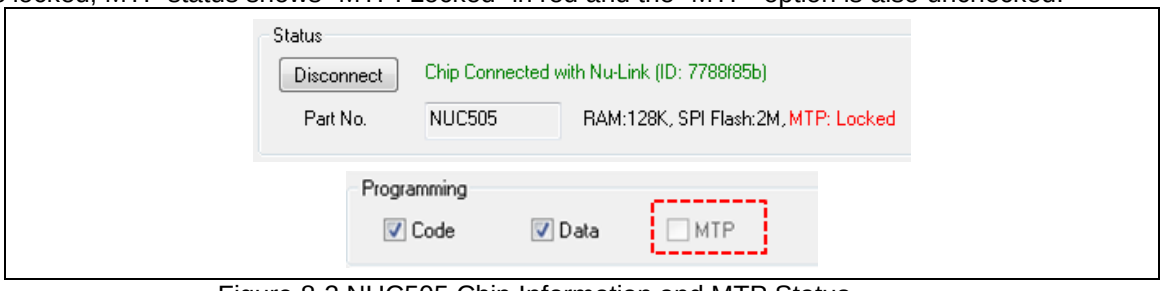

Figure 8-2 NUC505 Chip Information and MTP Status

Due to reading all contents of SPI Flash is time-consuming, partial-read from the assigned base address (Code Area is fixed at 0x0) and read size are provided (referring to section B and C in Figure 8-1). The "**Base Address**" and "**Read Size**" show different input format respectively. The "**Base Address**" is hex-coded, and "**Read Size**" is decimal-coded. User can read any range of SPI Flash by clicking "**Refresh**".

If MTP had ever been programmed or MTP is selected for this program, user can assign the file path (section D in Figure 8-1). When the programming process is ended, the ICP tool would merge the encrypted data of Code Area and Data Area into a binary file and save it to the assigned path. User can use ciphertext/plaintext binary to do ICP online/offline programming, based on whether target chip MTP key has been pre-programmed or not. In General, has to use ciphtertext when MTP has been pre-programmed. For more detail, please refer to "AN_0010_ICP_Programming_Guide".

Before starting to program a target board, user can select target blocks for programming (section E in Figure 8-1).

By clicking "**MTP Options**" (section F in Figure 8-1), user can open the MTP Option form and configure MTP related settings.

8.1.2 MTP Options

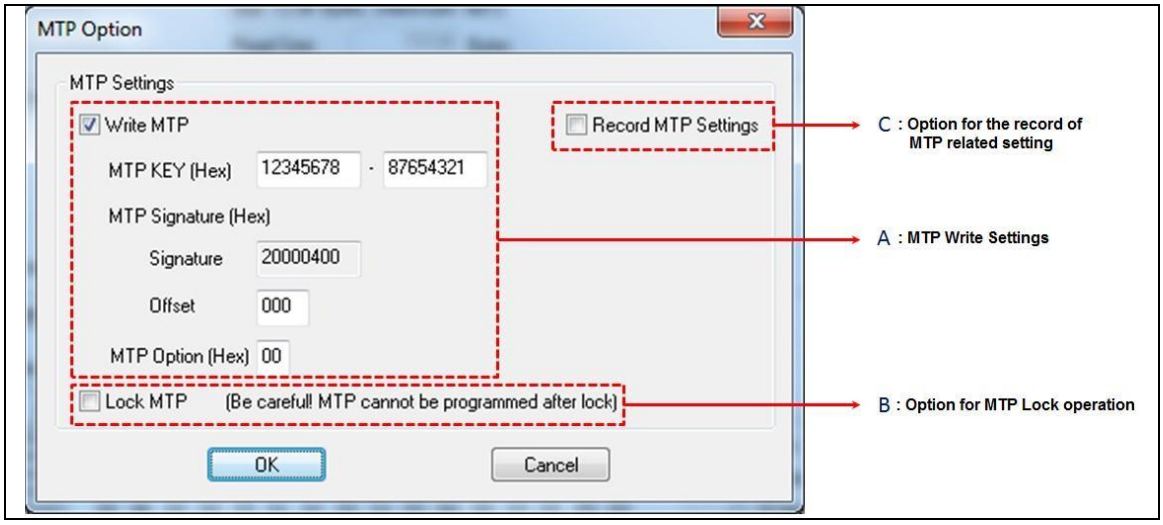

Figure 8-3 MTP Options

On the *MTP Option* form, user can select the desired operation and fill in MTP keys and signature. The options and fields are described below:

- "**Write MTP**": Write 64-bit key, 32-bit Signature, 12-bit Offset, and 1-byte Option with hex-code input format. If the file for Code Area is loaded, Tool will search the corresponding 32-bit value and fill in the "Signature" automatically according to the address of "Offset".

32-bit Signature is used to authenticate programmed binary, if the Signature on SPI Flash is not matched with user's input, system will boot up fail.

It is suggested to locate Signature after the end of interrupt vector table. (e.g. 0x100).

MTP Option (Hex) byte is another method to do authentication and is optional, user can ignore this if there is no special need.

- "**Lock MTP**": Lock MTP. MTP cannot be programmed after lock.
- "**Record MTP Settings**": If this option is checked, the MTP settings including MTP Key and Signature on MTP Option Dialog will be recorded after clicking "**OK**". When the ICP Tool is reopened, the previous MTP settings will be restored.

8.2 Support for M480 Series

8.2.1 Main Window for M480 Series

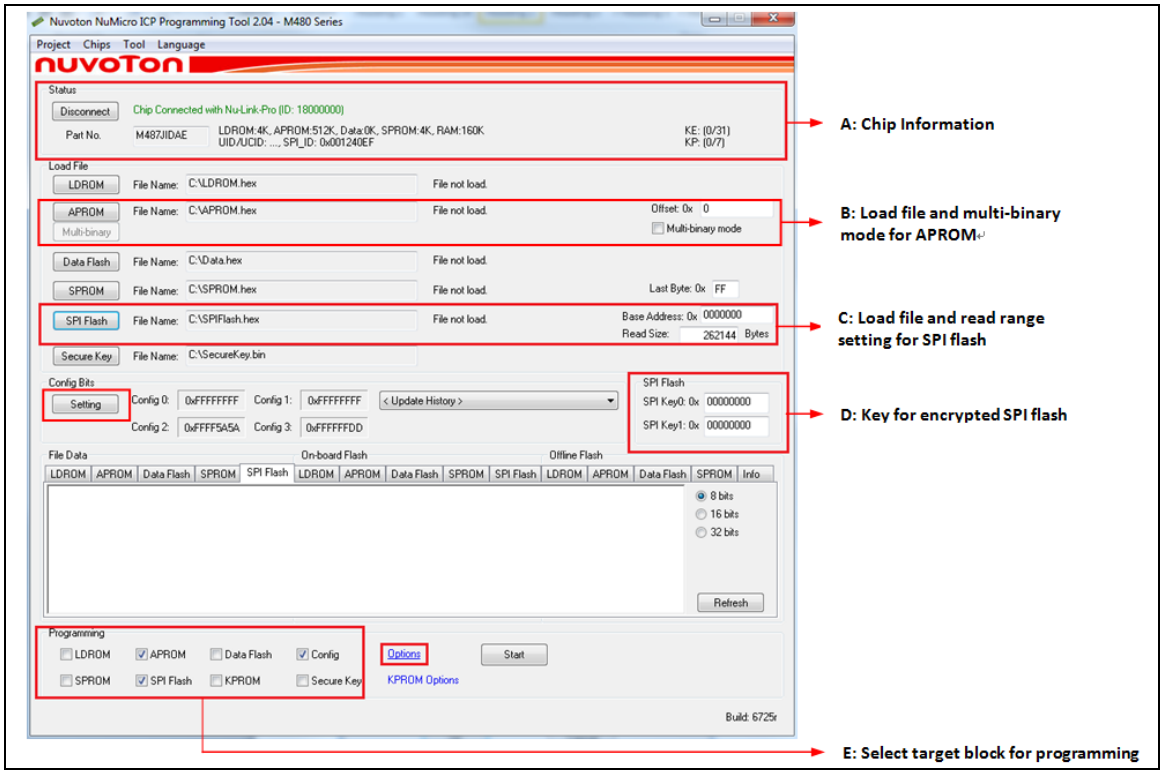

Figure 8-4 Main Window for M480 Series

Section A: Show the part no., LDROM, APROM, data Flash, SPROM, RAM size and SPI Flash ID of target chip information.

Section B: Load programming APROM file path or Load multiple binary file path using multi-binary mode.

Section C: Load programming SPI Flash file path and setting start address and read size for programming or reading SPI Flash. Due to reading all contents of SPI Flash is time-consuming, partial-read from the assigned base address and read size are provided. The "Base Address" and "Read Size" show different input format respectively. The "Base Address" is hex-coded, and "Read Size" is decimal-coded. User can read specific range of SPI Flash by clicking **"Refresh"** button.

Section D: User can assign SPI key0 and key1 to program encrypted file data to SPI Flash. If SPI key0 value is 0 or SPI key1 value is 0, ICP tool will not encrypt file data.

Section E: Select a target chip block for programming.

After clicking the “Connect” button, the ICP Tool would read chip information (including Part No., internal Flash size, RAM size, SPI Flash ID and KPROM status) and show the information. If SPI Flash is not detected, the value is 0xFFFFFFFF and the “SPI Flash” option is also unchecked in Figure 8-5.

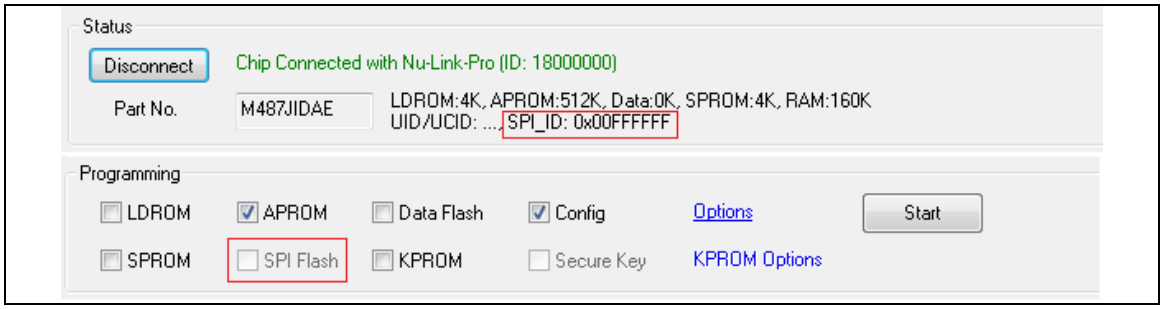

Figure 8-5 SPI Flash Status

Click config bits **“Setting”** button in Figure 8-4 and show the Chip Options form. In Chip Options form, user can select the SPIM multi–function pin option base on SPIM function pin status of target chip in Figure 8-6. After programming config of SPIM setting, ICP tool will detect the valid SPI ID value and enable **“SPI Flash”** option.

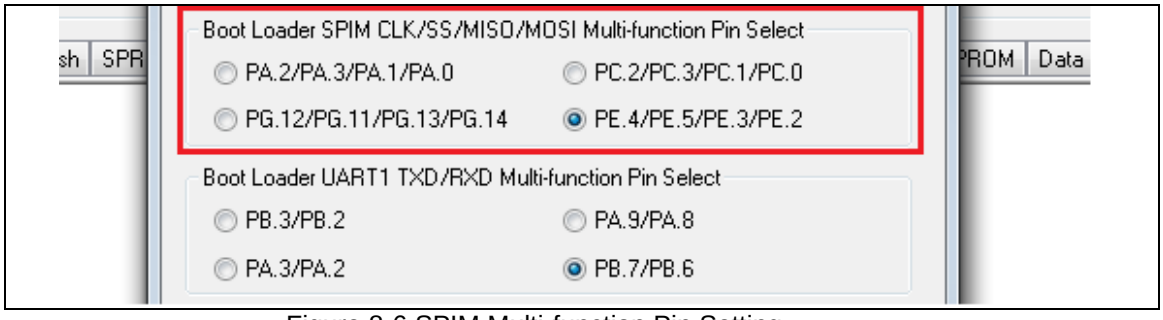

Figure 8-6 SPIM Multi-function Pin Setting

8.2.2 SPI Flash Programming

The ICP tool SPI Flash programming can be ciphertext or plaintext by assigned SPI key.
If SPI key0 or key1 value is 0, ICP tool will program data without encrypting binary file to SPI Flash. Due to the SPI Flash data is plaintext , user can verify SPI Flash data by checking the verify SPI option in Program Option form in Figure 8-7.

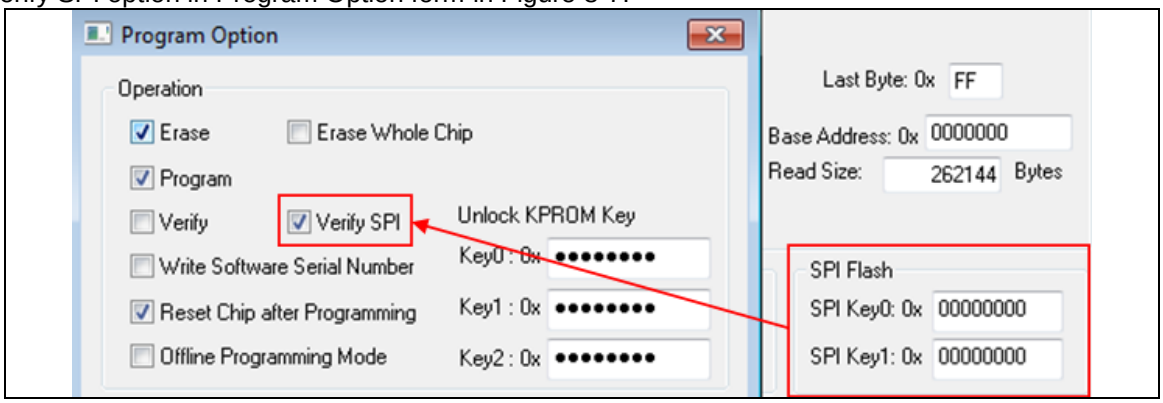

Figure 8-7 Plain Data Setting for SPI Flash Programming

If SPI key0 and key1 value is not 0, ICP tool will use SPI key0 and key1 to encrypt binary file and program to SPI Flash. Due to the SPI Flash data is ciphertext, the **"Verify SPI"** option disable in Program Option form in Figure 8-8.

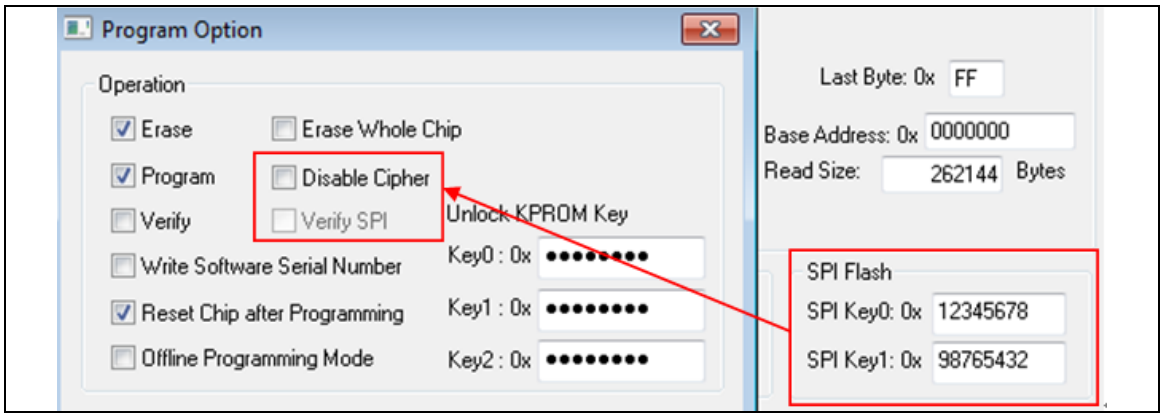

Figure 8-8 Encrypted Data Setting for SPI Flash Programming

User can also keep the SPI key value and program data to SPI Flash without encrypting binary file by checking the **"Disable Cipher"** option in Figure 8-8.

After programming ciphertext data to SPI Flash, User can save the encrypted SPI Flash data to file in Figure 8-9. Follow the steps below:

Step 1: Select "SPI Flash" On-board Flash tab.

Step 2: Click **"Refresh"** button and ICP tool read back the Flash data.

Step 3: Click **"Save As"** button and save ciphertext to binary file.

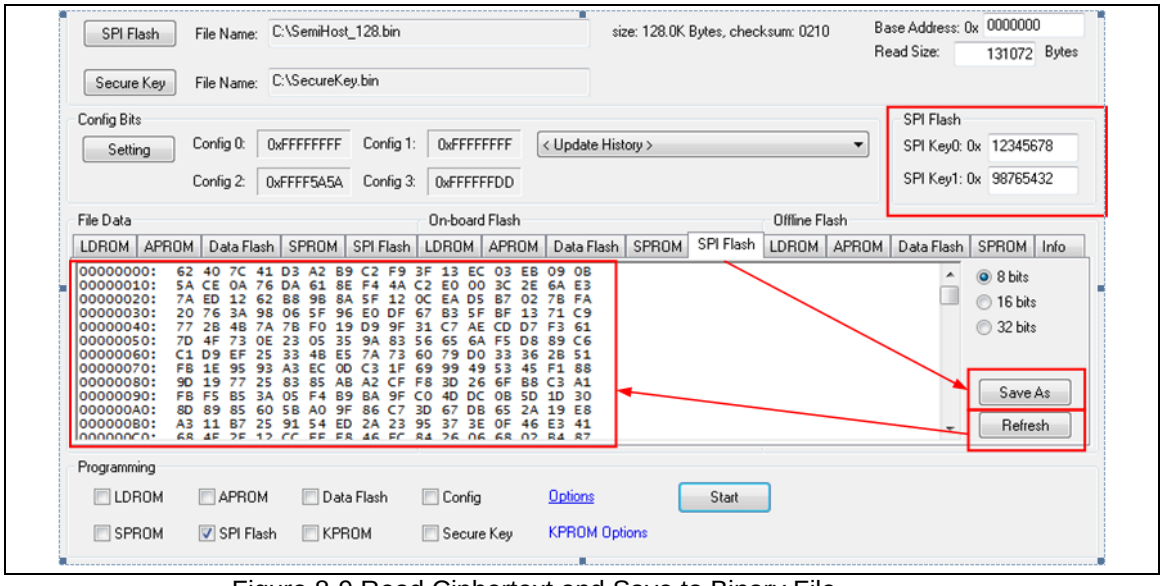

Figure 8-9 Read Ciphertext and Save to Binary File

8.2.3 KPROM Setting

ICP tool provide KPROM programming for writing protection of KPROM and APROM region. Select **"KPROM"** option in programming options and click **"KPROM options"** to set KPROM key setting in Figure 8-10.

Figure 8-10 KPROM Option

On the KPROM Key Setting form, user can select the desired operation and fill in KPROM keys and error key retry counts. KPROM keys are three 32-bit with hex-code input format. User can set the key0 ~ key2, error key retry counts and the optional write-protected region of Config or SPROM in Figure 8-11.

KPROM key setting will be recorded after clicking "OK" button. But the ICP tool is reopened, the previous KPROM key setting will not be recorded. User need to set the KPROM key setting again.

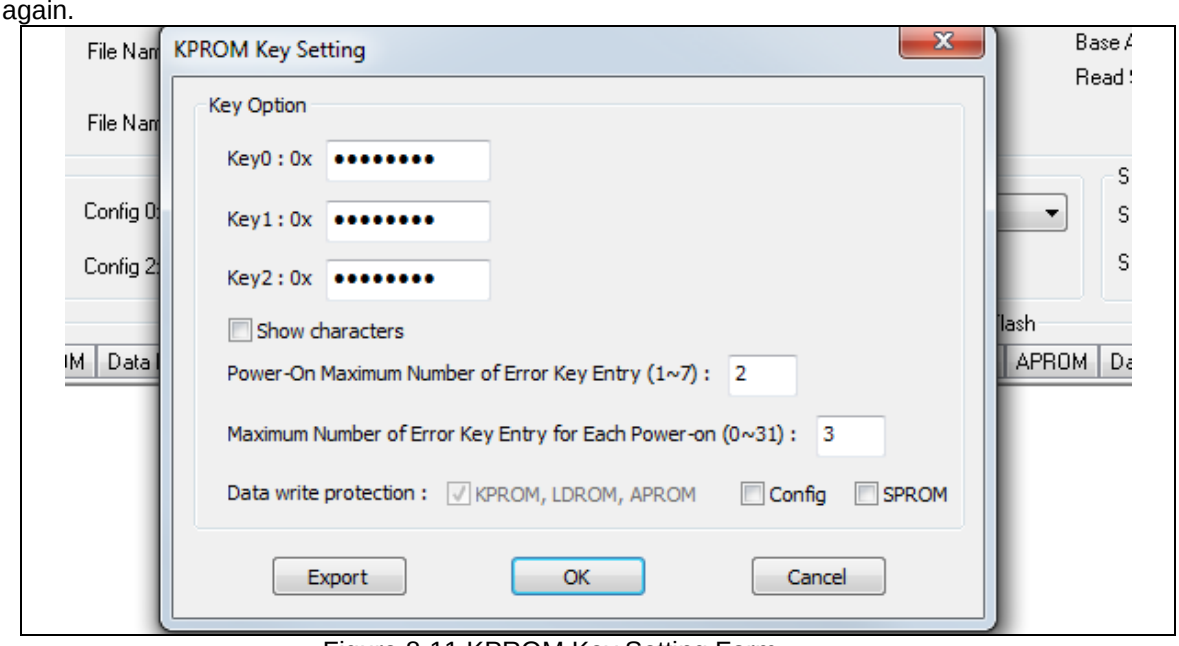

Figure 8-11 KPROM Key Setting Form

8.2.4 Secure Boot Key and Information Block Programming

ICP tool provide secure boot key and information programming. Secure boot key programming need to load secure boot key file, so user can generate secure boot key file by using **"Tool" -> "Create Secure Key File"** menu item in Figure 8-12.

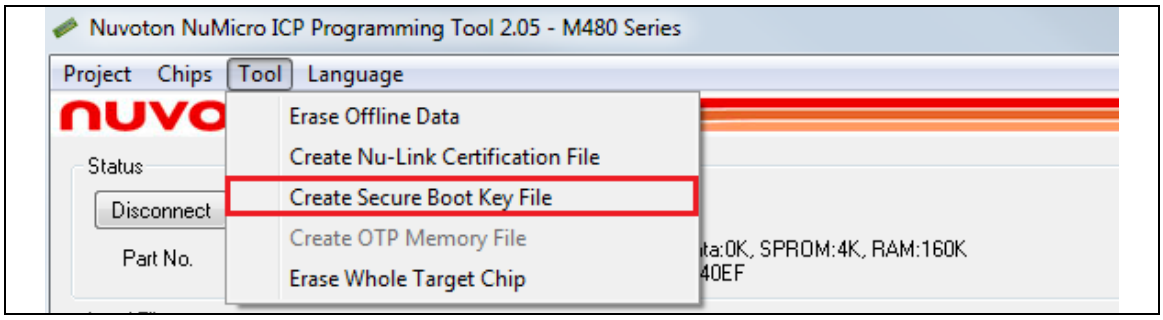

Figure 8-12 Create Secure Boot Key File Menu

On the Create Secure Key File form, user can select the desired operation in Figure 8-13. The options and fields are described below:

- "**Generate**": Random generate a secure boot key. User can also edit the key in key field.
- **"Export":** Save current secure boot key in key field to file.
- **"Import":** Select a secure boot key file and load key in the field.

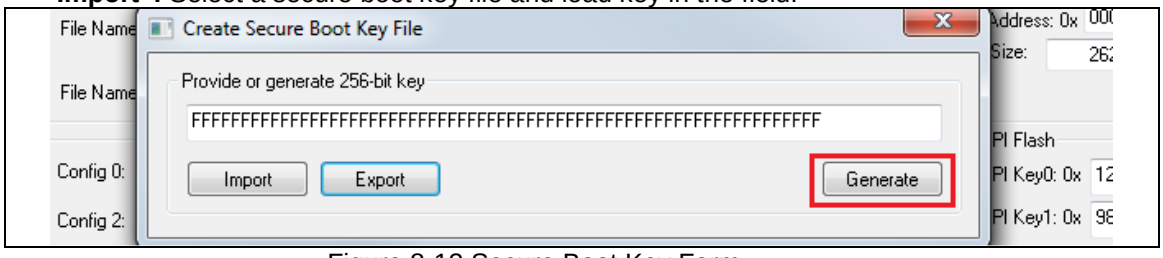

Figure 8-13 Secure Boot Key Form

To program secure boot key and information block, follow the steps below:

Step1: Click **"**Secure **Key"** button to load secure boot key file.

Step2: Select the target block in programming options and load the corresponding binary file of target block (LDROM, APROM or SPI Flash).

Step3: Click **"Setting"** button to enable **"boot Loader"** option in Figure 8-14

Step4: Select **"KPROM"** button and click **"KPROM Options"** to set KPROM key setting.

Step5: Select **"Secure Key"** option in programming options and click **"Start"** button to program in Figure 8-15.

Note: Once the **"Boot Loader"** option is enabled, Config, KPROM and Secure Key options must be enabled in programming options.

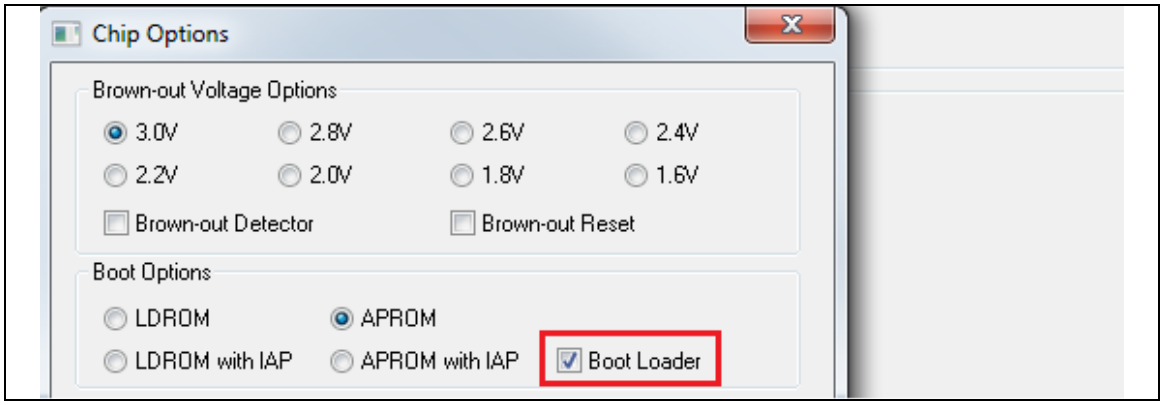

Figure 8-14 Boot Loader Option

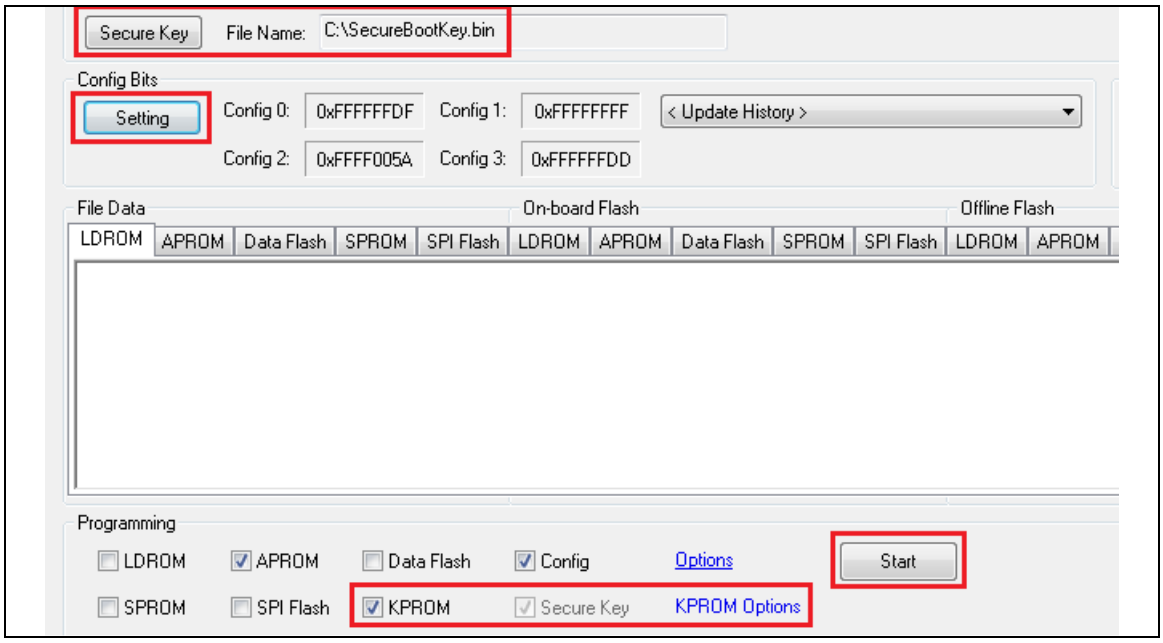

Figure 8-15 Program Secure Boot Key and Information Block

8.2.5 Multi-binary Mode

User can load multiple binary files to program APROM region separately.
In multi-binary mode, user needs to enable the "**Multi-binary mode**" option as shown in Figure 8-16.

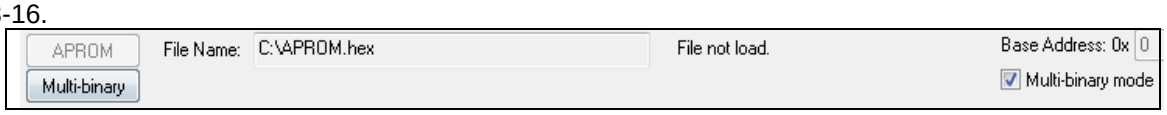

Figure 8-16 Multi-binary Mode

The "**Load Multiple Binary File**" form will be displayed after clicking the "**Multi-binary**" button as shown in Figure 8-17.

In "**Load Multiple Binary File**" form, user can load multiple files and set the start address and protection property.

- Use "**Add**" button to load a file and insert an item to the list.

- Use “**Remove**” button to delete an item from the list.
- Use “**Modify**” button to set offset value and protection column item to the list.
- Set the “**Offset**” value to define the start address of file programming.

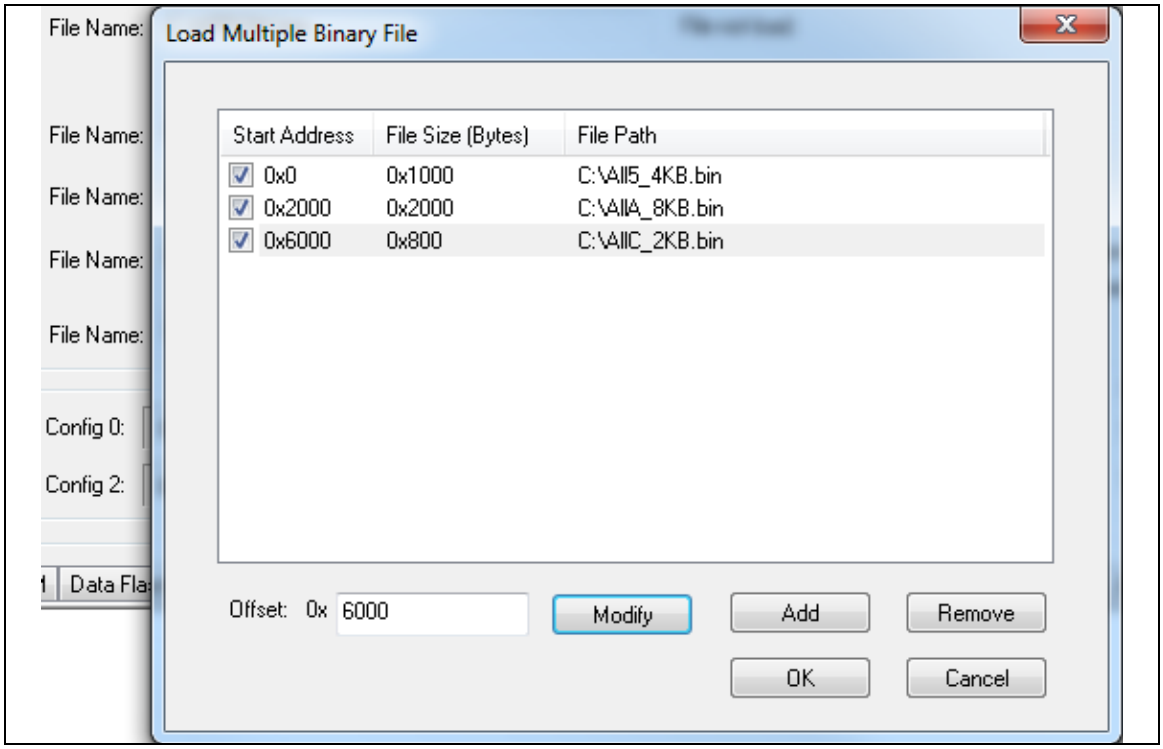

Figure 8-17 Load Multiple Binary Files

9 Revision History

Date	Revision	Description
2010.01.28	1.01.001	First version.
2010.02.24	1.01.002	Added offline programming mode.
2010.06.04	1.01.003	Changed key & LED definition.
2010.06.22	1.02.001	Added chip select and offline security.
2010.07.22	1.03.001	Supported M502x series.
2011.08.03	1.17.001	Supported Mini51/Nano100 series. Added more features in offline programming mode.
2012.08.10	1.19.001	Supported NUC200.
2013.07.01	1.20.001	Supported NUC200 and NUC123 series. Added firmware update and Nuvoton announcement.
2014.02.10	1.21.001	Changed document format.
2014.08.08	1.21.002	Added security chapter and reorganized document.
2014.09.05	1.22.001	Changed document and figure format.
2018.05.24	2.04.001	Added M480 series
2018.12.21	2.06.001	Changed document and figure format.
2020.11.27	3.05.001	Changed document and figure format.

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